Driving decarbonization and digitalization. Together.



AMS Design Engineer (f/m/div)

Job description

Join our team as an AMS design engineer, where you will design cutting-edge Analog-to-Digital Converters (ADCs) for diverse applications. In close collaboration with digital designers, you will perform detailed analysis of ADC specifications, develop sophisticated circuit schematics and conduct comprehensive simulations. Become an integral part of our team, a powerhouse of motivation and ingenuity. If you are passionate about pushing the boundaries of technology, we want to hear from you!

We are seeking a talented and experienced Analog Mixed-Signal Designer to join our team and focus on the design and development of high-performance Analog-to-Digital Converters (ADCs). You will be responsible for all aspects of the ADC design process, from concept development and architecture definition to design and simulation.

In your new role you will:

- Design high-performance, low-power, and high-resolution ADCs for various applications.
- Perform in-depth analysis of ADC specifications and translate them into circuit designs.
- Develop circuit schematics and perform simulations using industry-standard EDA tools.
- Collaborate with digital designers to ensure seamless integration of the ADC into the overall system.
- Analyze and optimize ADC performance metrics such as Signal-to-Noise Ratio (SNR), Effective Number of Bits (ENOB), and power consumption.
- Participate in **characterization and testing** of fabricated ADCs.
- **Stay up-to-date** on the latest ADC architectures, technologies, and design techniques.
- Document design decisions and maintain clear and concise design documentation

We are filling this position through one of our leasing partners. A valid work permit for Austria or EU citizenship is a prerequisite for this position. This position is subject to the collective agreement for workers and employees of EEI (https://www.feei.at/wp-content/uploads/2023/05/minimum-salaries-white-collar-workers-2024.pdf) with a minimum salary of € 4.215,39. The monthly salary is paid 14 times p.a. A higher payment is negotiable depending on your expertise and skills.

Profile

You are best equipped for this task if you have:

At a glance

Location:

Job ID: 104360
Start date: Nov 01, 2024
Entry level: 1-3 years
Type: Full time
Contract: Permanent

Apply to this position online by following the URL and entering the Job ID in our job search. Alternatively, you can also scan the QR code with your smartphone:

Job ID: 104360 www.infineon.com/jobs



Contact

Mag. Stefanie Triebelnig, BA



- PhD degree in Electrical Engineering or a related field.
- Proven experience in designing high-performance, low-power ADCs using CMOS or BiCMO S processes.
- Expertise in transistor-level circuit design and simulation techniques.
- Remarkable understanding of analog circuit theory, noise analysis, and signal integrity concepts.
- Strong analytical and problem-solving skills.
- Ability to work effectively in a team environment and communicate technical concepts clearly.
- Experience with industry-standard EDA tools (e.g., Cadence Virtuoso, Synopsys Custom Compiler) is a plus.

Benefits

Villach: Coaching, mentoring networking possibilities; Wide range of training offers & planning of career development; International assignments; Different career paths: Project Management, Technical Ladder, Management & Individual Contributor; Flexible working conditions; Home office options; Part-time work possible (also during parental leave); Sabbatical; Child care in Villach & Klagenfurt; On-site social counselling and works doctor; Health promotion programs; On-site canteen; Private insurance offers; Wage payment in case of sick leave; Corporate pension benefits; Flexible transition into retirement; Performance bonus; Accessibility, access for wheelchairs

Why Us

Driving decarbonization and digitalization. Together.

Infineon designs, develops, manufactures, and markets a broad range of semiconductors and semiconductor-based solutions, focusing on key markets in the automotive, industrial, and consumer sectors. Its products range from standard components to special components for digital, analog, and mixed-signal applications to customer-specific solutions together with the appropriate software.

The central research and development (R&D) organization " **Design Enabling and Services" (DES)** provides the design environments (design-systems) to the different product development teams at Infineon. With leading edge design methods, complex building blocks and a wide range of product development services DES enables Infineon's advanced IC development. This means the complete value creation chain from abstract system models down to the fully verified product layout which ensures high quality manufacturing readiness.

At Infineon in Villach you shape the technologies of tomorrow and work in an international environment with more than 4700 colleagues from over 70 nations. Your personal contribution will be valued and appreciated as the cornerstones of our success. And all that in beautiful surroundings which guarantee a high quality of life.

The **City of Villach** is located in the center of Carinthia, Austria's southernmost province, in close proximity to the Italian and Slovenian border. Living in Austria has many social, health-care-related and economic perks. The country's social and health care system is among the best in the world and for decades numerous international surveys have singled out Austria as a particularly safe and wealthy country with a high quality of life. Villach itself benefits from its status as a "small town", offering everyday living at affordable prices in an outstanding setting.

Find out what you like most about Villach and join us: https://www.welcome2villach.at/

We are on a journey to create the best Infineon for everyone.

This means we embrace diversity and inclusion and welcome everyone for who they are. At Infineon, we offer a working environment characterized by trust, openness,



respect and tolerance and are committed to give all applicants and employees equal opportunities. We base our recruiting decisions on the applicant´s experience and skills.

We look forward to receiving your resume, even if you do not entirely meet all the requirements of the job posting.

Please let your recruiter know if they need to pay special attention to something in order to enable your participation in the interview process.

Click here for more information about Diversity & Inclusion at Infineon.

