

TRAVEO™ T2G CYT4DN MCU series

Providing unparalleled excellence for automotive clusters and graphics

Infineon releases its second generation TRAVEO™ microcontroller in embedded flash 40 nm technology. It comes back with an increase in graphic performance, memory sizes and connectivity to address the new automotive trends and challenges. CYT4DN offers one of the most scalable safety and graphic microcontroller portfolios. In terms of performance, it offers dual core Cortex-M7 running up to 320 MHz, up to 6 MBytes embedded Flash, 1920x720 display graphics in a 327-ball BGA package. It is consuming below 707 mA in Active mode and 50 uA in DeepSleep mode with 32 KB RAM retention. Its mirrored embedded flash banks offer A/B swap capabilities.

Graphics subsystem supports 2D and 2.5D (perspective warping, 3D effects) graphics rendering up to 24-bit color resolution (RGB). CYT4DN provides one capture engine for video input processing for ITU 656 or parallel RGB/YUV or MIPI CSI-2 input and up to two video output interfaces supporting two displays from parallel RGB, FPD-link single/FPD-link dual. JPEG Decoder decodes JPEG images of various formats into pixel data with conformance to a subset of standard ISO/IEC10918-1.

Safety is the core know-how of Infineon, and all products provide safety mechanism (including MBIST, ECC Flash/RAM, CRC) to ensure a safety platform supporting ASIL-B ISO 26262. State-of-the-art security with Secure Boot support by a dedicated ARM® Cortex®-M0+ core and security hardware to accelerate cryptographic functions.

In terms of security, this product has an HSM compliant Evita full, ensuring the implementation of future proofed security measure. On top of this, it offers extensive connectivity with 4 CAN FD, 2 LINs, two SPIs and new high-speed communicating interfaces such as one Gbit Ethernet and two CXPIs.

The TRAVEO™ CYT4DN offers single chip solution up to 1920x720 display with enhancing 2.5D graphics engine. On-The-Fly rendering mode and hardware decompression enables the most cost-efficient smart Cockpit without high-end 3D SoC or complicated software. And it features a dedicated best-in-class standby mode controller, with its own voltage domain to, not only support low power modes, but also to perform certain operations such as analog measurements, CAN and LIN communications, RTC and basic processing while the rest of microcontroller is in standby.



Key features

- ARM® Cortex®-M7 dual CPU up to 320 MHz operation
- Up to 6 MB flash, 128 KB work flash, 640 KB SRAM
- 2.5D graphics engine 720p, 4M VRAM, Vector drawing, JPEG decoder
- Video-out: 2-ch (LVDS, RGB)/ Video-In: 1-ch (RGB, MIPI)
- 2x Mixer, 5x Sound Generator, 2x PCM-PWM, DAC
- Up to 1-ch Gbit Ethernet, 4-ch CAN-FD, 12-ch SCB, 2-ch LIN, 2-ch CXPI and 2-ch SMIF
- Cortex®-M0+ for HSM security

Key benefits

- Single chip graphic solution up to 1920x720 display
- Optimized memory footprint for reduced BOM
- Best-in-class performance enabling ASIL-B designs
- A/B swap software update over the air support
- Best-in-class power consumption
- TRAVEO™ T2G graphic MCU portfolio for a wide range of applications and a high level of software re-use

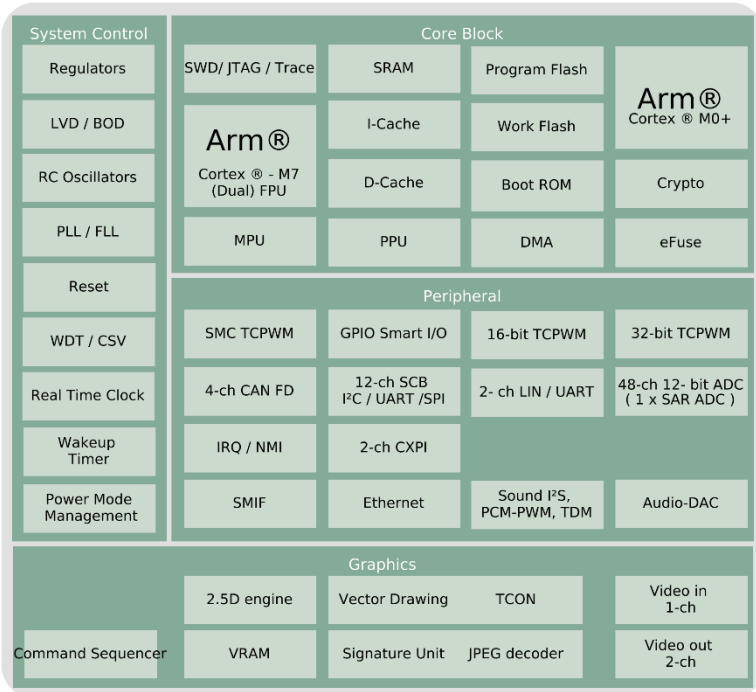
Key applications

- Instrument cluster
- HUD
- Lighting system
- Cost effective cockpit solution



PRODUCT BRIEF

Block Diagram



Product table

Device Code	Ordering Code	Package	CMT Cores	Code-flash (KB)	Work-flash (KB)	RAM (KB)	ADC Channels	SCB	CAN FD	LIN	CXPI	Ethernet Channels	SMIF	Audio DAC	MIPI	JPEG Decoder	Temperature Grade	JTAG ID CODE
CYT4DNJBRS	CYT4DNJBRCQ1BZSGS	327-BGA	2	6336	128	640	48	12	4	2	2	1	2	1	1	Yes	105°C	0x2E820069

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